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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/154,237 05/23/2002 PAT 6,716,747

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-153326 05/23/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 2	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature Initials				

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## TITLE

Method of manufacturing a semiconductor apparatus with a tapered aperture pattern to form a predetermined line width

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